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DRL No. 109
DRD No. SE-2

DOE/JPL - 955404 - 81/1
Distribution Category UC-63

(NASA-CR-165007) DESIGN, FABRICATION, TEST,
QUALIFICATION, AND PRICE ANALYSIS OF THIRD
GENERATION DESIGN SOLAR CELL MODULES Final
Design Report (Solarex Corp., Rockville,
Md.) 33 p HC A03/MF A01

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FINAL DESIGN REPORT

DESIGN, FABRICATION, TEST, QUALIFICATION, AND
PRICE ANALYSIS OF "THIRD GENERATION" DESIGN
SOLAR CELL MODULES

BLOCK IV

JPL Contract No. 955404

October 15, 1981



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SOLAR CELL MODULES**

BLOCK IV

JPL Contract No. 955404

October 15, 1981

**SOLAREX CORPORATION
1335 Piccard Drive
Rockville, MD 20850
(301) 948-0202**

The JPL Low-Cost Solar Array Project is sponsored by the U.S. Department of Energy and forms part of the Solar Photovoltaic Conversion Program to initiate a major effort toward the development of low-cost solar arrays. This work was performed for the Jet Propulsion Laboratory, California Institute of Technology by agreement between NASA and DOE.

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1.0 Description of Program

The Solarex Block IV contract calls for the design, manufacture, and delivery of eighteen residential load modules and eighteen intermediate load modules. Common features of both modules include:

- 72 9.5 cm X 9.5 cm Semicrystalline Cells
- Cells - Ti-Pd-Ag front metallization with n/p p+ junction
- Geometrically arranged in 6 X 12 matrix
- 3/16" Sunadex tempered glass superstrate
- Ethylene vinyl acetate as encapsulant with craneglas spacer
- White Tedlar moisture barrier
- Redundant Cell-Interconnect Design which has six pads per cell
- Wraparound Interconnect
- Circuit Board style interconnect with in-plane stress relief feature.
- Two pigtail connections per positive or negative outlet

Features which are different are shown in Table I.

TABLE I - DESIGN FEATURES

	Intermediate	Residential
	Load Module	Load Module
Outside Envelope Dimensions (cm)	63.5 x 120	62.8 x 119.3
Series-Parallel Arrangements	2P - 36S	6P - 12S
Design Voltage (Volts)	14	4.5
Diode Protection	One per two parallel cells	Two per six parallel cells
Support	Aluminum-modified channel	Supplied without frame

A drawing list is included as Sections 7.0 and 8.0. Top Assembly drawings are included in 7.1 and 8.1.

2.0 Program Plan

The program was divided into six tasks:

Task I - Program Management. This task consists of the effort necessary to manage the contract and insure that contract deliverables are on schedule.

Task II - The manufacture, test, and delivery to JPL of the reference solar cells.

Task III - The building, check-out, and de-bugging of a prototype module fabrication machine to test lamination techniques. Complete design drawings for full-scale module fabrication machine.

Task IV - Fabricate, environmentally test, and deliver to JPL ten preproduction modules (five residential and five intermediate load).

Task V - Fabricate and deliver to JPL 26 (13 residential and 13 intermediate load) completed modules.

Task VI - SAMIS/SAMICS support. Effort, computer time and travel necessary to do SAMIS/SAMICS.

3.0 Schedule

The schedule of the program plan (both planned and actual) is shown in Table II. Almost all tasks were delayed. The prime reasons are:

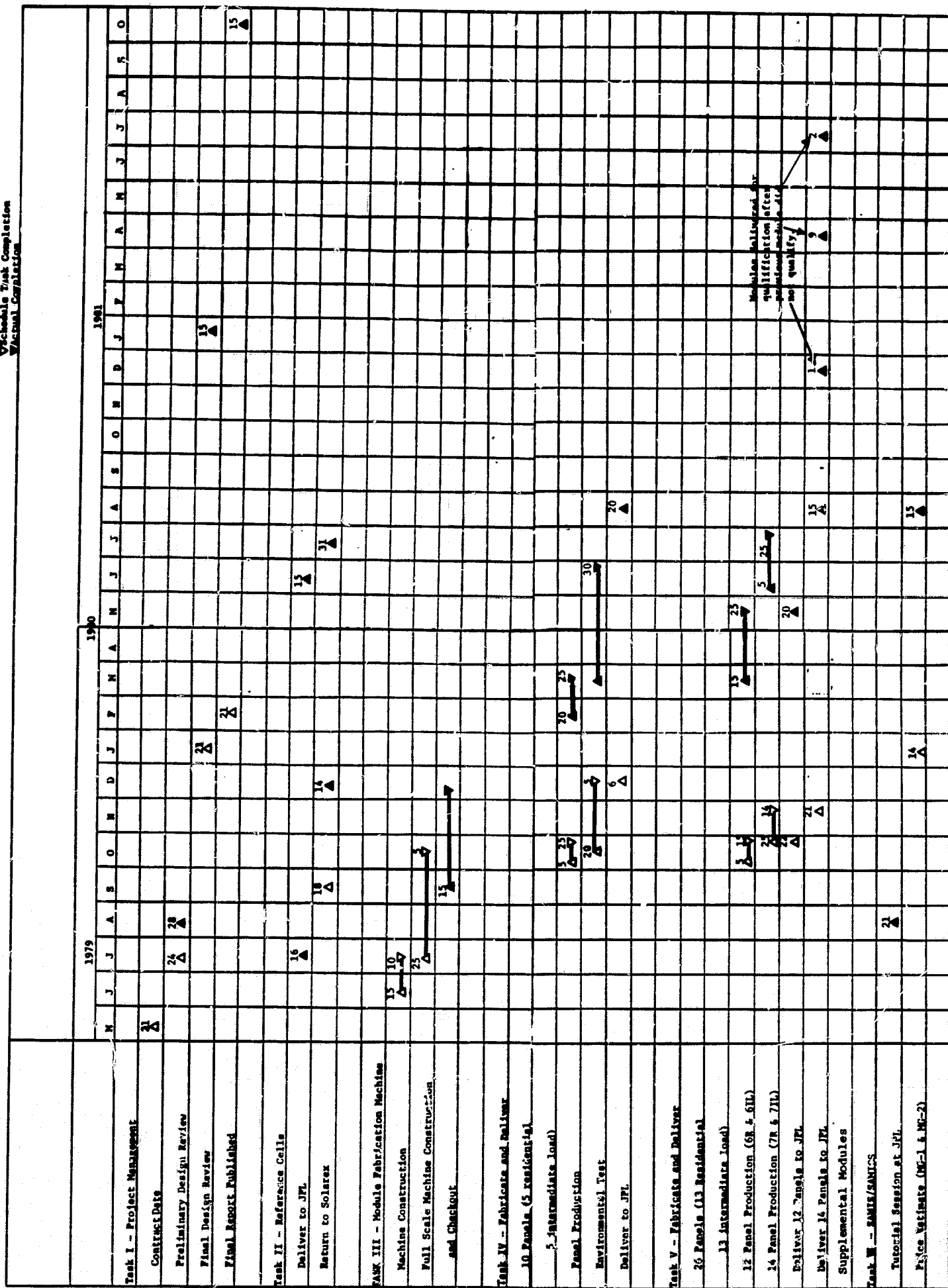
- Start-up problems with cell manufacturing. Cells originally scheduled for the Block IV contract were to be semi-crystalline cells with Ni-Solder metallization. The start-up problems associated with this cell line caused the entire schedule to slip. Later, to prevent any further schedule slip, we made the decision not to use Ni-Solder metallization.
- Various problems that showed up during environmental testing. The Block IV contract was designed to allow both Solarex and JPL the opportunity to environmentally test the modules. In the first test, at both Solarex and JPL, there was a problem with delaminations during thermal cycling. This problem was solved by applying a primer/adhesive (Dupont 68040) to the Tedlar prior to lamination. In December, 1980, a second set of modules was sent to JPL for testing and there were other problems. Specifically there was delamination of the cell back metallization, power loss during humidity testing, and some cell movement during thermal cycling.

It was determined that the reason for those problems was the cell back metallization that was not resistant to humidity (and subsequently caused delamination and some power loss). The cause for the cell movement during thermal cycling was inadequate EVA cure. The cell back metallization was changed to a Tin-Silver alloy and the lamination procedure was changed to give a more complete cure. At

the time this was done it was not well understood what constituted an adequate cure.

- The third set of panels submitted to JPL exhibited yet another problem. There was EVA/Tedlar delamination that showed up during thermal cycling. However the delamination was primarily restricted to the area around the junction boxes. We determined that the most probable cause of this delamination was the diffusion of outgassing products from the silicon adhesive (that forms the bond between the Tedlar and the junction boxes) to the EVA/Tedlar bond. This weakens the bond enough so that it sometimes delaminates during thermal cycling. A suitable fabrication procedure that works consists of making an initial "priming" application of RTV-102, allowing it to cure, and then applying another coat of RTV-102 both to the primed surface and the junction box.
- The fourth set of panels submitted to JPL passed qualification.
- Throughout the qualification program there has been a problem with ripples or wrinkles in the Tedlar and EVA on the back. Though this problem was not cause for rejection, it nevertheless decreased the cosmetic appeal of the module. Another persistent problem was the lack of a consistently good edge seal. Bubbles tended to form near the edge and the edge was uneven, again decreasing the cosmetic appeal and introducing a possible moisture path. Both of these problems have been solved by a general improvement of techniques during the lamination process. The production run of the modules is much superior in all respects.

Δ Schedule
 ▲ Actual
 ▽ Schedule Task Completion
 ▽ Actual Completion



4. Environmental Tests

Environmental tests were performed on modules at two different locations. Task IV involved Solarex testing five intermediate load and five residential load modules. JPL also performed environmental tests on a number of modules. The basis for testing is specified in:

JPL-5101-83-"Block IV Solar Cell Module Design and Test Specification for Residential Modules".

and

JPL-5101-16 Rev A - "Block IV Solar Cell Module Design and Test Specification for Intermediate Load Center Applications".

The environmental test procedures consisted of:

- Thermal Cycling - 50 six-hour cycles for -40°C to $+90^{\circ}\text{C}$.
- Humidity - as specified in above mentioned documents.
- Mechanical Cycling - 10,000 cycles - positive and negative 50 lbs/ft^2 load.
- Twisted Mounting Surface - as specified in above documents.
- Hail Impact - Modules must be capable of withstanding $3/4"$ diameter iceballs traveling at velocity of 45 mph.

Solarex sub-contracted the thermal cycling, humidity, and mechanical cycling tests to an independent testing laboratory, the Stanford Technology Corporation, of Glenbrook, Conn. Solarex did the twisted mounting surface test at its facility. The hail test was done only at JPL.

5.0 Design Alternations During Pre-Production Phase

5.1 Interconnect Design:

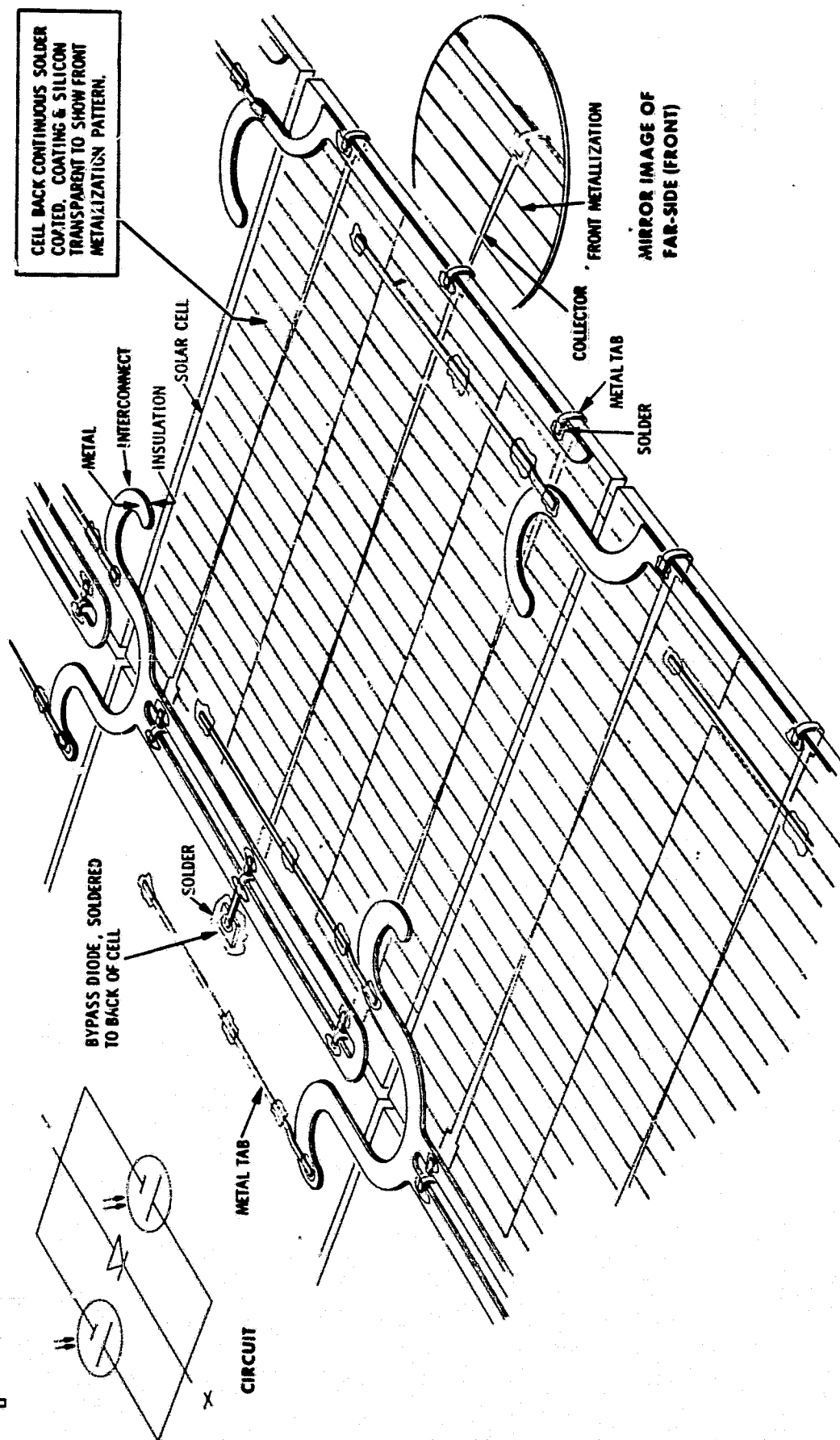
For Block IV, a new type of interconnect (terrestrial applications) was developed. A companion cell design was developed that was compatible with the wraparound concept. A sketch of the interconnect (as drawn by JPL) is shown in Figure 1.

The original idea was that there should be six connections on the front and six connections on the back to provide a highly reliable design. However, with the original design, the interconnect connection to the back of the cell was such that it was about an inch in-board from the connections on the front. We decided to alter the design such that the front and back electrical connections would be about the same relative location on the front and back. The design that is in use is shown on Figure 2.

5.2 Cell Metallization

Two changes were made in cell metallization during the preproduction phase. Solarex's initial intention was to use Ni-Solder. However, problems in transferring this technology from single crystal to the larger 9.5 cm square semicrystalline cells prevented us from achieving this objective.

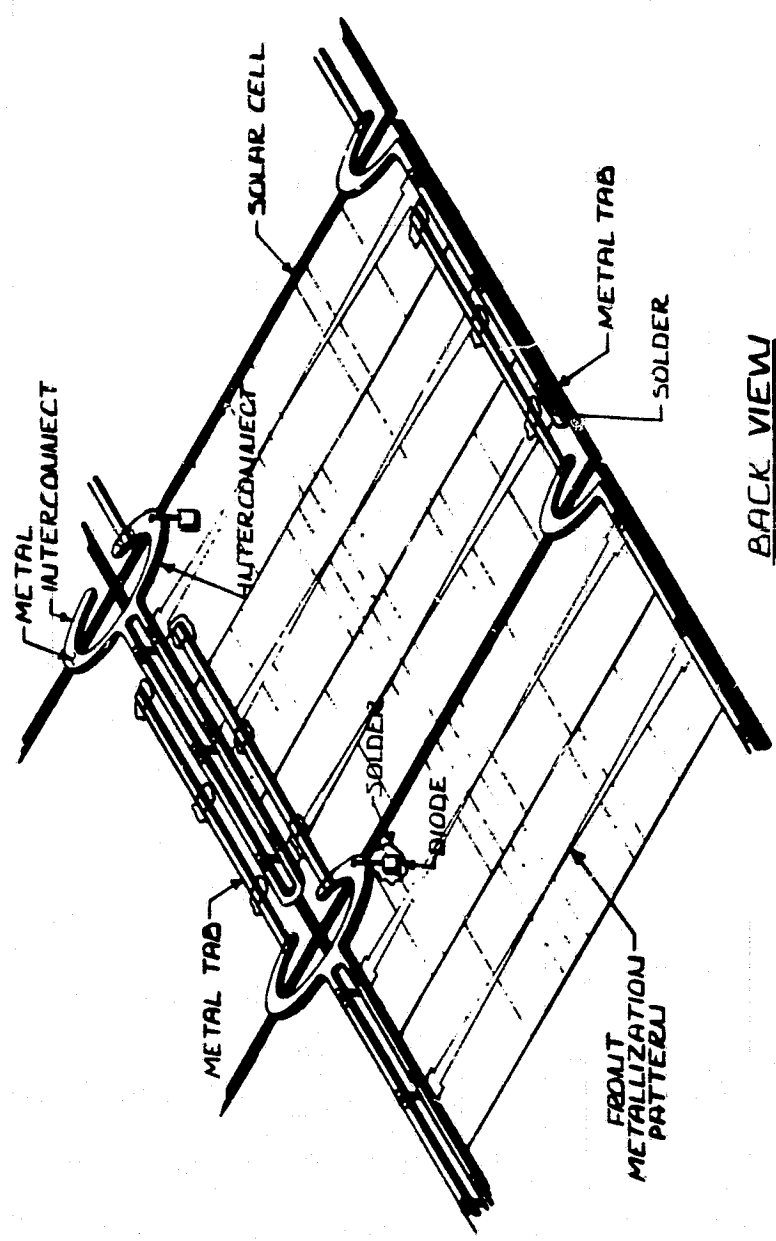
To prevent the schedule from being delayed any longer, we used the tried-and-proven Ti-Pd-Ag front metallization on the Block IV cells. Because of a problem that occurred during humidity testing at JPL in April 1981, a decision was made to use a Tin-Silver alloy back. Production



**SOLAREX CELL/INTERCONNECT SYSTEM SHOWN FROM BACK OF CELLS
JPL BLOCK IV MODULES**

1 2 3 4

REVISIONS		
ZONE	DESCRIPTION	DATE
1		



BACK VIEW

FIG. 2 - INTERCONNECT CELL-DIODE ILLUSTRATION

SOLAREX CORPORATION 1350 MCCARD DRIVE ROCKVILLE, MD 20850		CELL / INTERCONNECT SYSTEM	
		SIZE CODE SHEET NO. 989	DRAWING NO. 989
CONTRACT NO.		DATE 1-13-61	
APPROVALS DRAWN BY K. L. R.		CHECKED	
PRACTICING ENGINEER		ENGINEER	
USED ON		APPLICATION	
NEXT ARMY		DO NOT SCALE DRAWING	

Block IV modules have Ti-Pd-Ag fronts and Tin-Silver over Aluminum backs.

Secondly, in keeping with the desire to have a highly reliable and fault tolerant design, we changed the metallization pattern to allow redundant current paths should the primary path be interrupted due to cell breakage or other opens. The mask pattern is shown on drawing B-0813.

5.3 Frame Design - Intermediate Load

The original design was a one-piece design with welded corners. One of the narrow ends was "split" to allow some flexibility to ease the glass into the frame. We had problems with this for two reasons. First, opening the frame to insert the glass called for extraordinary care or the corner welds would develop cracks. Secondly, the welding workmanship of the vendor was not always up to par and despite extraordinary care, cracks still developed. We remedied this problem by making the frame in two pieces with two L-brackets joining the two sides. The resultant frame is a much sturdier, easier-to-construct frame.

The frame and L-Bracket drawings are shown on D-0928-1, D-0929-2 and D-0864.

5.4 Lamination Procedure

Our initial lamination machine and manufacturing procedure was based on the work done by Springborn Laboratories under contract to JPL.

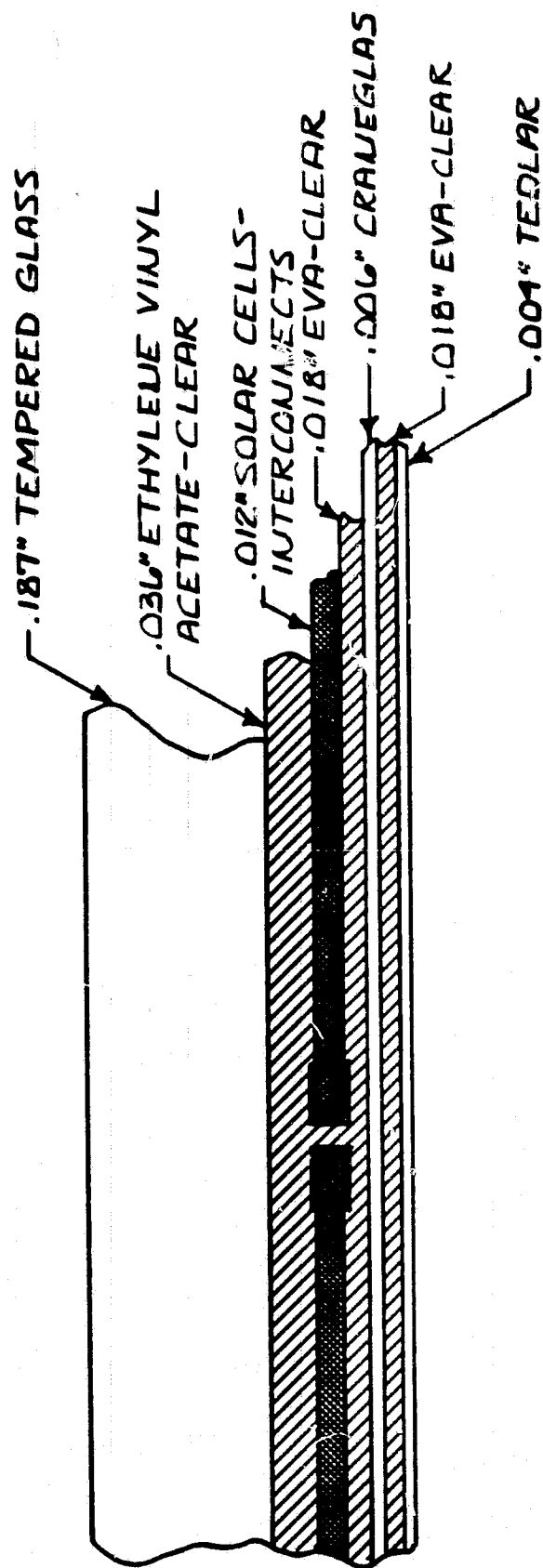
However, we had difficulty in consistently making bubble-free full-size panels. Our first approach was to make the panels in two steps. The first lay up consisted of the glass, two layers of EVA and the cell-interconnect

layer. In the second lamination, the final layer of EVA and one layer of Tedlar were added and put through the lamination cycle. While this approach did achieve a good EVA cure, it had two major disadvantages. Too many cells were broken and the cycle time was excessive. When the modules were subjected to thermal cycling, there was excessive delamination between the Tedlar and EVA.

Our initial approach to resolving these problems was to use much less pressure during lamination. To accomplish this, instead of an air bag technique, we used weights. This decreased the pressure from one atmosphere (about 14.7 psi) to the equivalent of about 0.14 psi. We also introduced a one step lamination procedure. The results were mixed: the cycle time decreased markedly, cell breakage was eliminated but we still had problems with bubbles, especially near the edges. The cosmetic problems with wrinkly backs continued. We tried many variations of lamination methods to improve the quality and found that the best results were attained by using a pressure differential of about 1/2 atmosphere (~7 psi) with an air bag. We also installed better control and monitoring instrumentation both for the electrical and pressure/vacuum systems.

The procedure that is being used for the production run results in a module that is bubble-free, rarely has cell breakage, has a well-defined, bubble free edge and withstands environmental testing.

Figure 3 shows a sketch of materials used presently.



ENCAPSULATION CROSS-SECTION

6.0 Performance Characteristics

Characteristics of the module as measured or determined by JPL are:

	<u>INTERMEDIATE LOAD</u>	<u>RESIDENTIAL LOAD</u>
NOCT - °C*	56	56
Avg. Power - @ 56°C-watts	56.0**	56.0***
P min - watts	50.4	50.4
$\Delta V/\Delta T$ - Volts/°C	-0.0856	-0.029
$\Delta I/\Delta T$ - Amps/°C	+0.0045	+0.0136


* NOCT Insolation value = 100 mw/cm²

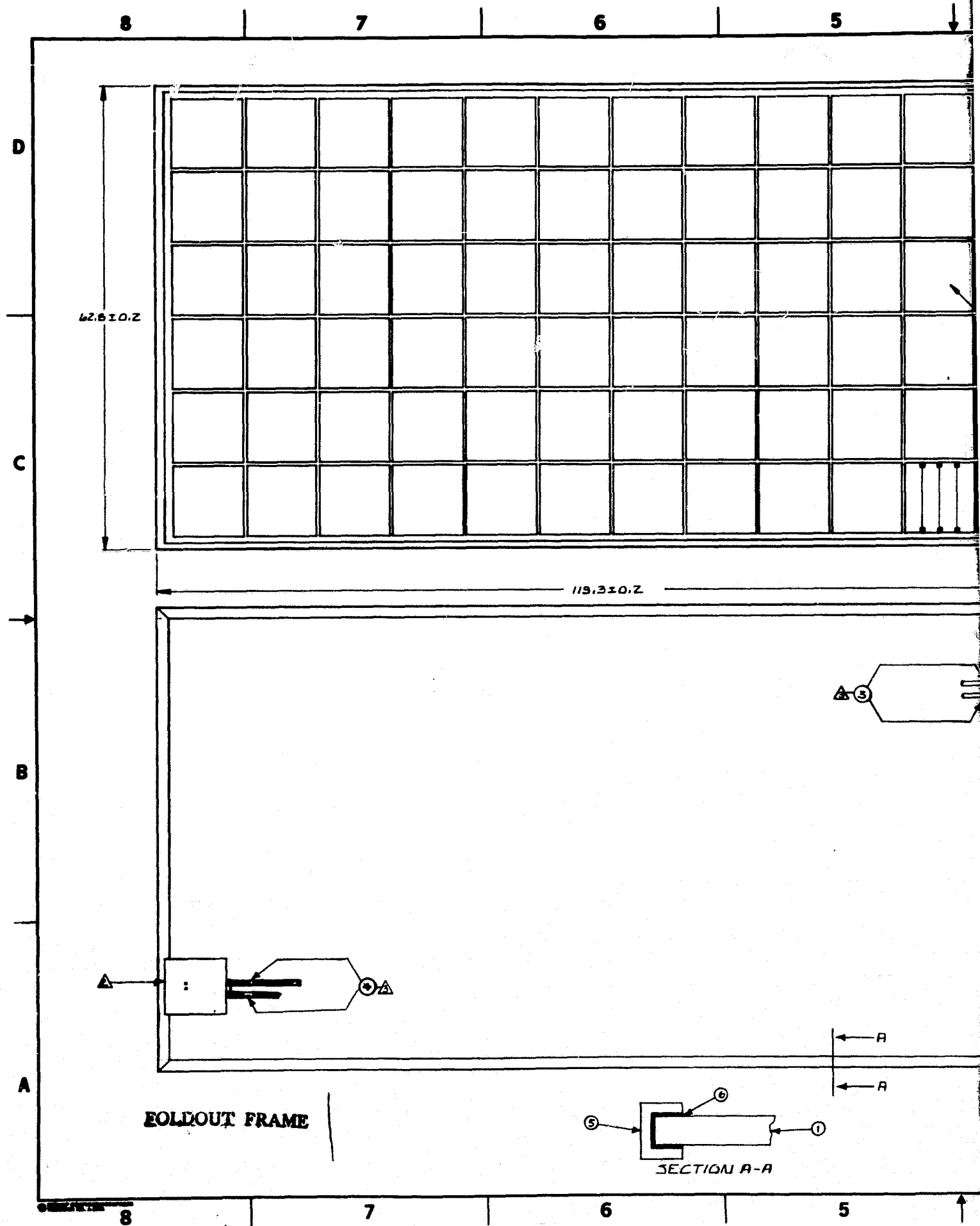
** Design Voltage = 14.0 Volts

*** Design Voltage - 4.5 Volts

APPLICATION		REVISION			
NEXT ASSY	USED ON	LTR	DESCRIPTION	DATE	APPROVED
		A	0865F, 0863D	3-2-81	JFH
		B	0863E, 0865G	3-19-81	JFH
D-0865G	Block IV Residential Load Top Assembly				
D-0975B	Block IV Residential Lamination Assembly				
D-0871E	Block IV Residential Load Cell Assembly				
*B-0872A	Solar Cell Tab Assembly				
*B-0813A	B-4 Solar Cell Mask				
*C-0926	Block IV Interconnect				
C-0927-1,					
-2A	Block IV R & L Interconnect				
B-0825C	Electrical Schematic - Residential Load				
*B-0870A	Block IV Terminal Box Detail				
*B-0879	Identification Label				
A-0976A	Gasket - Block IV Residential				
D-0863E	Interface Control Residential Load - Block IV - 2 Sheets				

*Applicable to both Residential and Intermediate Modules

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES ± .XX ± ± .XXX ±	CONTRACT NO.		 SOLAREX CORPORATION 1335 PICCARD DRIVE ROCKVILLE, MD 20850 □ 301 946 0202		
	APPROVALS	DATE			
	MATERIAL	DRAWN		Block IV Residential Load Drawing List 15	
	FINISH	CHECKED			
DO NOT SCALE DRAWING	SIZE A	CODE IDENT NO.	DRAWING NO. 580-BT-R-B		
	SCALE		SHEET	OF	



REVISIONS				DATE	APPROVED
ZONE	LTR	DESCRIPTION			
A		GENERAL REVISIONS		1/19/80	JFH
B		GENERAL REVISION		9/19/80	JFH
C		PROVISION FOR GASKET		11/19/80	JFH
D		62.5 ± 0.2 WAS 62.2		12/9/80	JFH
		119.3 ± 0.2 WAS 116.7			
E		GEN. REV.		1/30/81	JFH
F		ADD PROV. FOR MODIFICATION-LABEL		9/2/81	JFH
G		CHANGE ▲ NOTE		3/14/81	JFH

ASSEMBLY NOTES

▲ ID LABEL - IN MODEL NO. BLOCK TYPE FOLLOWING INFO:
580-SE-4-250 V RM.
WHERE N IS REVISION NUMBER ON DRAWING LIST 580-SE-4

IN SERIAL NO. BLOCK TYPE FOLLOWING INFO:
XXXXXX - YY - ZZ
XXXXXX = NO ASSIGNED FROM SOLAREX KESTER CONTROL LOG
YY = CALENDAR YEAR
ZZ = SEQUENTIAL WEEK OF YEAR

PEEL OFF BACK PROTECTIVE PAPER AND APPLY ON POSITIVE
TERMINAL BOX WITH TOP IN DIRECTION SHOWN

▲ GASKET TRIMMED TO FIT FLUSH WITH TERMINAL BOX
▲ APPLY POLARITY LABELS

FOLDOUT FRAME

QTY	REQD	CODE	IDENT	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	MANUFACTURER
6	1/R			RTV-108	ADHESIVE	C.E.
5	1			0976	GASKET	PAWLING RUBBER CO.
4	2			LABEL	NEG	SOLAREX
3	2			LABEL	POS	SOLAREX
2	1			R4D-0879	IDENTIFICATION LABEL	SOLAREX
1	1			R4D 0975	LAMINATION ASS'Y	SOLAREX

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES ARE: FRACTIONS DECIMALS ANGLES
± .XX ± 0.1 ±

CONTRACT NO.

APPROVALS DATE
DRAWN BY: JFH 8/29/79
CHECKED BY: JFH 8/29/79

SOLAREX CORPORATION
1335 PICCARD DRIVE ROCKVILLE, MD 20850 □ 301 948 0202

BLOCK III RESIDENTIAL LOAD
TOP ASSEMBLY

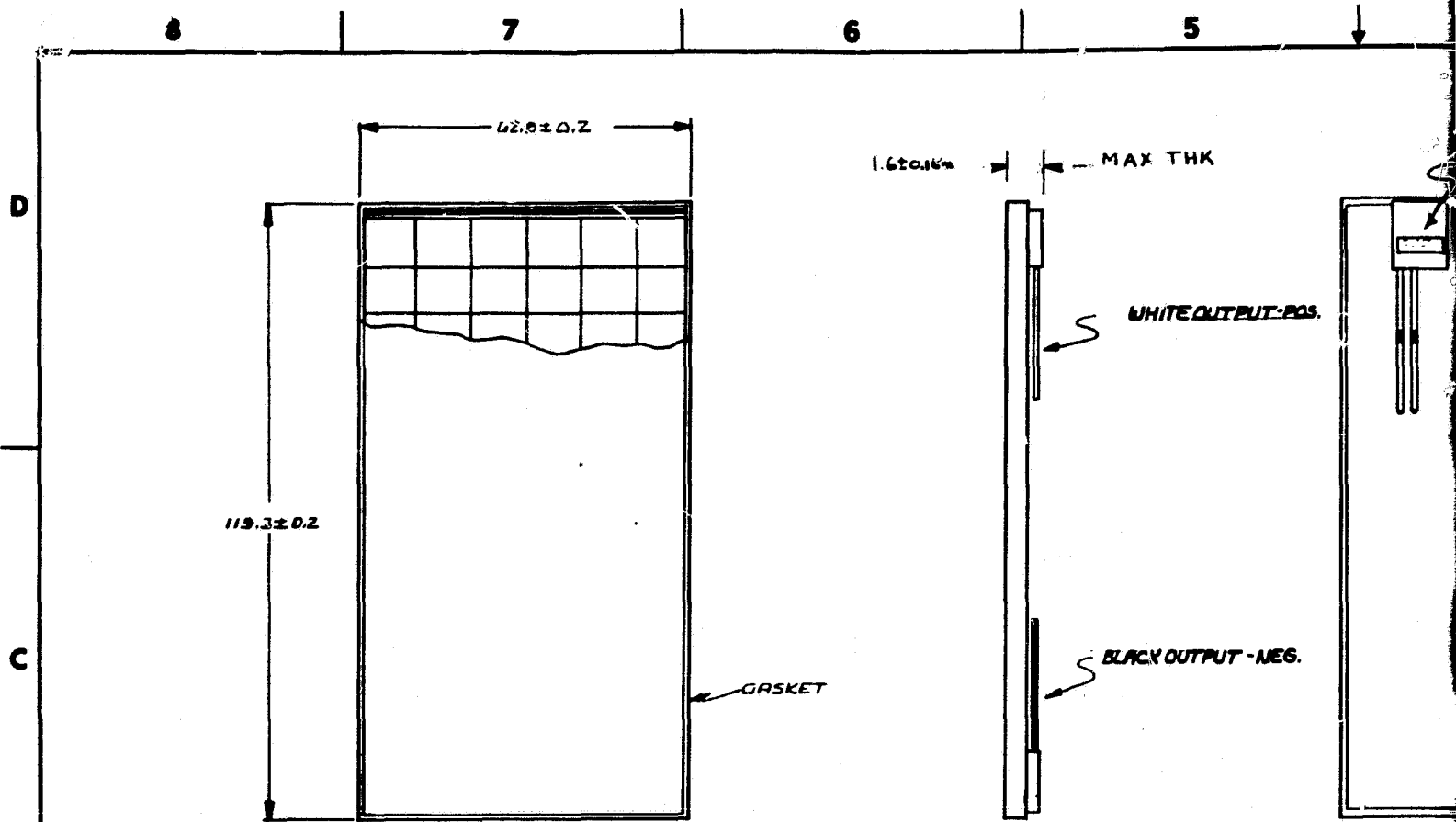
Section 7.1

SIZE CODE IDENT NO DRAWING NO.
D R4D 0865G

SCALE 1:3 SHEET 1 OF 1

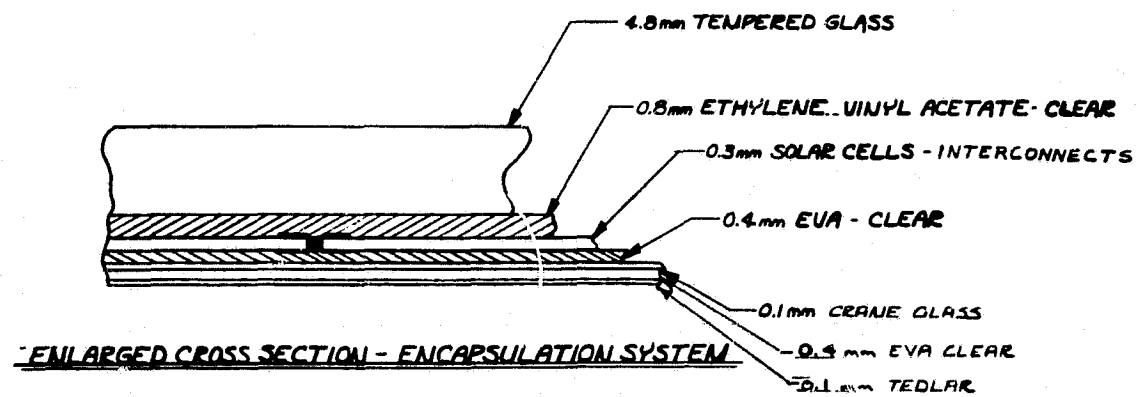
DO NOT SCALE DRAWING

APPLICATION NEXT ASSY USED ON



TOP VIEW

SIDE VIEW



ENLARGED CROSS SECTION - ENCAPSULATION SYSTEM

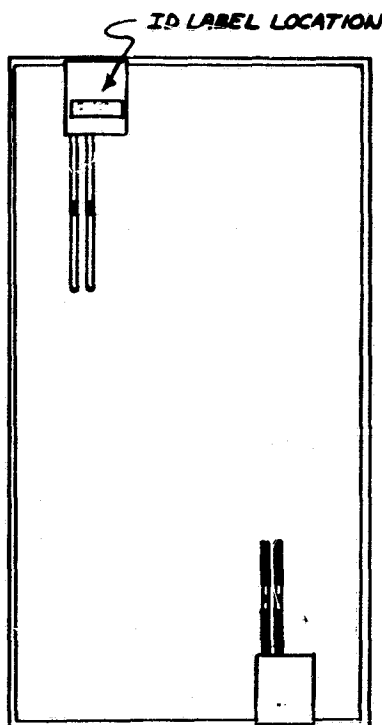
FOLDOUT FRAME

REVISONS			
ZONE	LTR	DESCRIPTION	DATE
	A	GEN. REVISION	12/04/80
	B	GEN. REVISION	12/25/80
	C	GEN. REV.	1/30/81
	D	ADD PROV. FOR MODIFICATION LABEL	3/2/81
	E	CHANGE LABEL REV. CONVENTION	5/19/81
			JFH

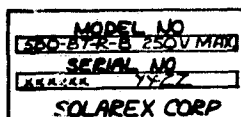
NOTES:

1. THE SOLAREX BLOCK IV RESIDENTIAL MODULE HAS NO INTEGRATED FRAME. ONE METHOD OF MOUNTING IS SHOWN HERE.

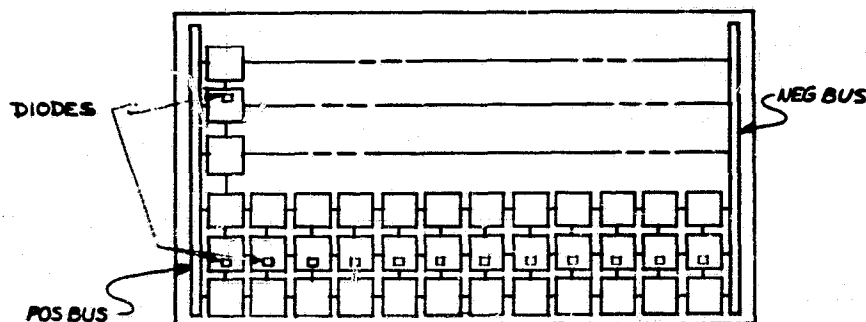
- Two positive and negative #12 WIRES FROM EACH OF TWO TERMINAL BOXES -- BLACK - NEG. WHITE - POS.
- ACTIVE (ILLUMINATED) SURFACE DIMENSIONS (WITH 1/2" OVERLAP FOR GASKET) = 59.7 CM X 116.2 CM.
- MAX WT -- WITHOUT FRAMES = 30 LBS.
- MODULE SHALL NOT BE TWISTED MORE THAN 1/4" PER FOOT.
- I.D. LABEL ON POSITIVE TERMINAL BOX. TOP TOWARD SHORT DIM.
- IN MODEL NUMBER BLOCK REFERS TO MODEL REVISION NUMBER.
- SERIAL NO BLOCK ON LABEL REFERS TO SEQUENTIAL NO OF MODULES AND YEAR AND WEEK OF MANUFACTURE.
- XXXXXX = SEQUENTIAL SERIAL NO
- XY = CALENDAR YEAR
- YY = WEEK IN CALENDAR YEAR
- ALL WIRE LENGTHS -- 30 CM FROM TERMINAL BOX.
- TWO DIODES PER SIX PARALLELED CELLS.
- NO SHADOWING WHEN SUN ANGLE IS GREATER THAN 76.2 DEGREES - LONG DIMENSION OR 87 DEGREES - SHORT DIMENSION (GASKET-RUBBER MOLD DESIGN.)



REAR VIEW



ID LABEL -



CELL ORIENTATION - BACK VIEW
(NOT TO SCALE)

QTY REQD	CODE IDENT	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION
PARTS LIST			
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN CM. TOLERANCES ARE AS SHOWN. FRACTIONS DECIMALS ANGLES		CONTRACT NO.	
= .XX = =		APPROVALS DATE	
= .XXX = =		DRAWN P. MEYER 12/25/80	
MATERIAL		CHECKED C. 11-2-1 12/25/80	
FINISH		Section 7.2	
NEXT ASSY USED ON		SHEET 1 OF 2	
APPLICATION		DO NOT SCALE DRAWING	

SOLAREX CORPORATION 1335 PICCARD DRIVE ROCKVILLE, MD 20850 □ 301 948 0308	
INTERFACE CONTROL RESIDENTIAL LOAD BLOCK IV	
SIZE D	CODE IDENT NO. R1D
DRAWING NO. 0863E	

FOLDOUT FRAME

8

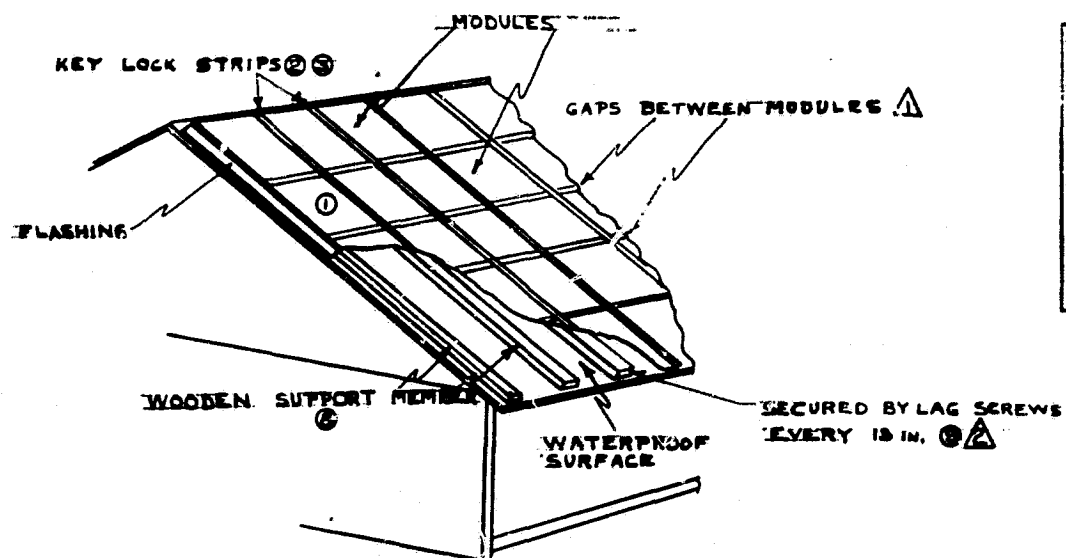
7

6

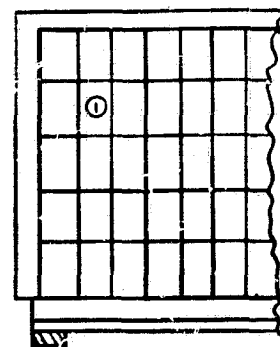
5

D

MOUNTING CONCEPT

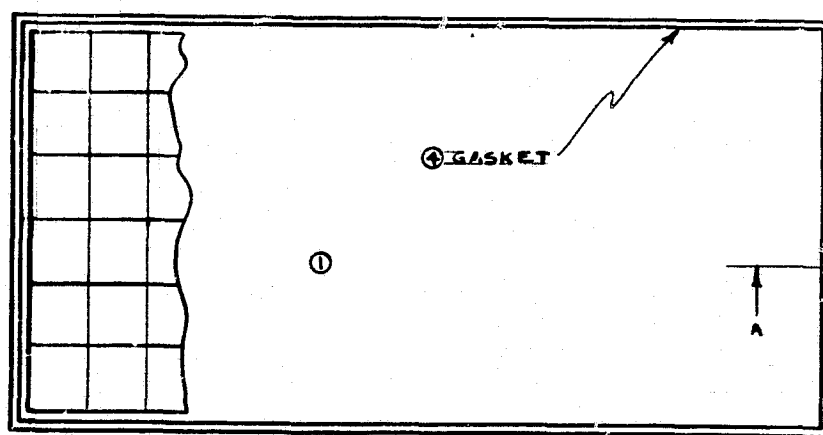


FRONT VIEW



C

B

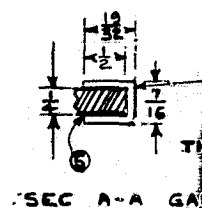


TOP VIEW - SHOWING GASKET DIM.

A

FOLDOUT FRAME

PROV FOR 3

SEC B-B K
AND SUPPORT

SEC A-A GA


8

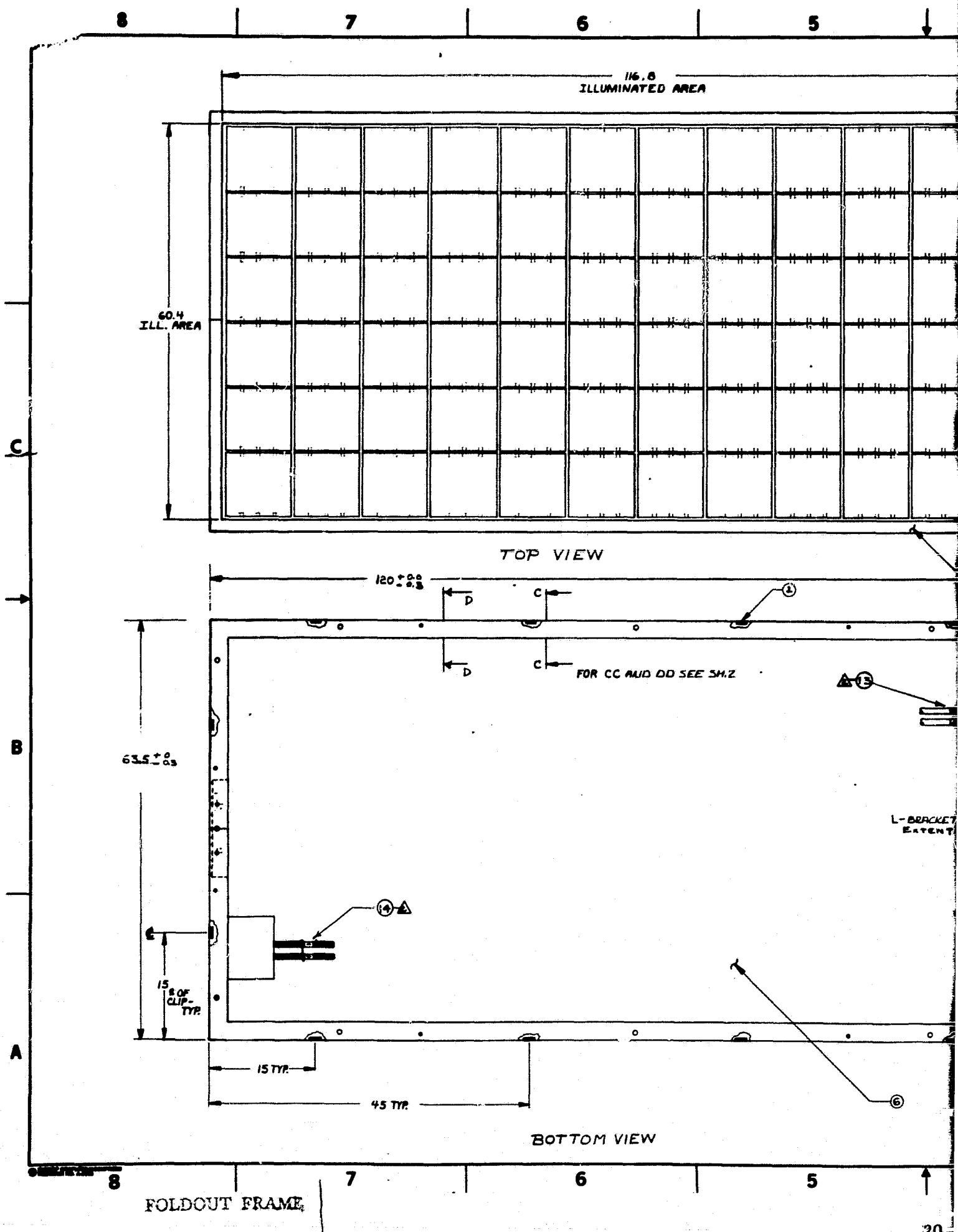
7

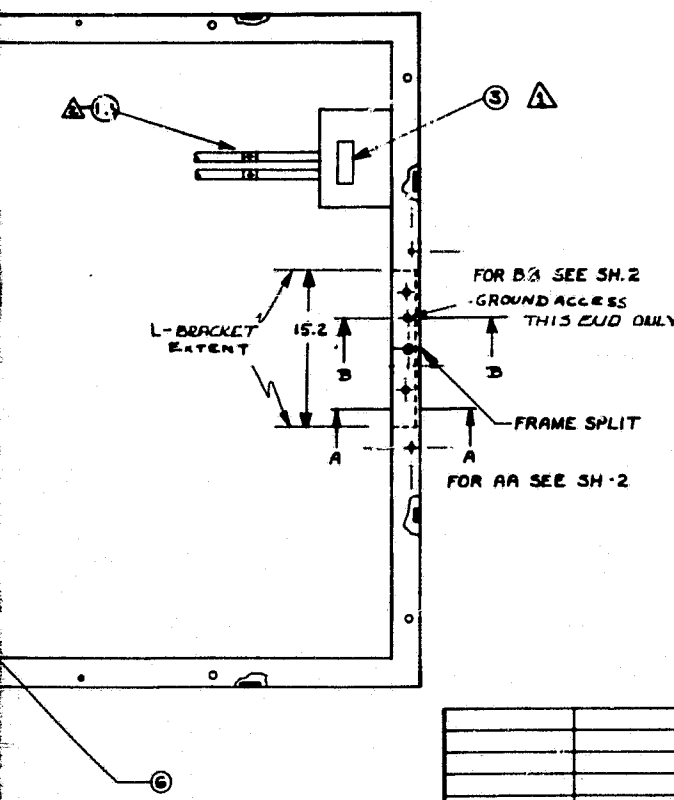
6

5

APPLICATION		REVISION			
NEXT ASSY	USED ON	LTR	DESCRIPTION	DATE	APPROVED
		A	0864F, 0862E	3-2-81	JF H
		B	0862F, 0864G, D-0928A, C-0929A	3-19-81	JF H
D-0864G	Block IV - Intermediate Load Top Assembly - 2 Sheets				
	D-0928A-1, Frame - MIT Lincoln Laboratory -2				
	C-0929A L - Bracket - Frame - MIT Lincoln Laboratory				
A-0852A	Rear Support Clip				
D-0866E	Block IV - Intermediate Load Lamination Assembly				
	D-0867E Block IV - Intermediate Load Cell Assembly				
	*B-0872A Solar Cell Tab Assembly				
	*B-0813A B-4 Solar Cell Mask				
	*C-0926 Block IV Interconnect				
	*C-0927-1, -2A Block IV Right & Left Interconnect				
	*B-0870A Block IV Terminal Box Detail				
	B-0826C Electrical Schematic - Intermediate Load				
	*B-0879 Identification Label				
D-0862F	Interface Control - Block IV Intermediate Load				
*Applicable to both Residential and Intermediate Load Modules					

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES ± .XX ± ± .XXX ±	CONTRACT NO.		 SOLAREX CORPORATION 1335 PICCARD DRIVE ROCKVILLE, MD 20850 □ 301 948 0202			
	APPROVALS	DATE				
	MATERIAL	DRAWN		Block IV Intermediate Load Drawing List 19		
		CHECKED				
FINISH			SIZE	CODE IDENT NO.	DRAWING NO.	
			A		580-BT-L-B	
DO NOT SCALE DRAWING			SCALE		SHEET OF	





REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
	A	GEN REVISION	12/4/79	JFH
	B	GEN REVISION	9/10/79	JFH
	C	GEN. REVISION	9/22/80	JFH
	D	GEN. REVISION	12/27/80	JFH
	E	EXPAND ASS'Y NOT.	1/30/81	JFH
	F	ADD PROV. FOR MODIFICATION-LABEL	3/22/81	JFH
	G	CHANGE ▲ NOTE	3/11/81	JFH

ASSEMBLY NOTES

Frame assembly- Lay completed lamination assembly, (0046) on flat surface (glass side down) place two frame sides (0928-1,-2) around lamination assembly, and secure with L-brackets (0929). With and ground provision is nearest positive terminal). Turn frame sides lamination assembly, over the lamination assembly, and install the rubber tubing on the inside of the frame back. Apply cautious bead of RTV-106 on outer 1/2" of glass surface. Turn glass-frame down over (glass-side down) and insert rear support clips (0052). Clean excess RTV-106 from glass and frame. Apply white RTV-102

⚠ ID LABEL - IN MODEL NO BLOCK-TYPE FOLLOWING INFO:
500-BT-L-W 500 V MAX.
 WIND. W IS REVISION NUMBER ON DRAWING LIST 500-BT-L

IN SERIAL NO BLOCK TYPE FOLLOWING INFO:

XXXXXX - YY - 22


THE 1995-1996 FLOOD DAMAGE IN THE CHANGJIANG BASIN

XX - CALENDAR YEAR

22 - SEQUENTIAL WEEK OF YEAR

2 **APPLY POLARITY LABELS**

15	AIR		RTV-102 WHITE	G.E.
14	Z	LABEL	NEGATIVE	
13	2	LABEL	POSITIVE	
12	AIR	ADHESIVE	RTV 108-CLEAR	GEN. ELECTRIC
11	8	FLAT WASHER	* 8- STAINLESS STEEL	
10	1	STARWASHER	* 8- STAINLESS STEEL	
9	10	NUTS	* 8-32 NC-SS HEX	
8	1	MACHINE SCREW	* 8-32-1/4 NC-SS 82° FLT HEAD	
7	8	MACHINE SCREW	* 8-32-1/4 NC-SS 82° FLT HEAD	
6	1	R-D 0866	LAMINATION ASSEMBLY	SOLAREX
4	1	R-D 0928-1,-2	FRAME-MIT LINCOLN LAB.	SOLAREX
3	1	R-D 0879	IDENTIFICATION LABEL	SOLAREX
2	Z	R-D 0929	L-BRACKET-FRAME-MIT LIN. LAB.	SOLAREX
1	12	R-D 0852	REAR SUPPORT CLIP	SOLAREX

NO.	QTY REQD	CODE IDENT	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	MANUFACTURER
PARTS LIST					
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN <u>CM</u> TOLERANCES ARE: FRACTIONS DECIMALS ANGLES = = = JXX = JXX =			CONTRACT NO.  SOLAREX CORPORATION 1335 PICCARD DRIVE ROCKVILLE, MD 20850 □ 301 948 0202		
MATERIAL			APPROVALS		DATE
			DRAWN <u>P. MEAGNER</u>		<u>11/30/79</u>
			CHECKED <u>H. B. J.</u>		<u>11/1/79</u>
FINISH			BLOCK II INTERMEDIATE LOAD TOP ASSEMBLY		
Section 8.1			SIZE	CODE IDENT NO.	DRAWING NO.
			D	R4D	086YG
DO NOT SCALE DRAWING			SCALE		SHEET / OF 2

8

7

6

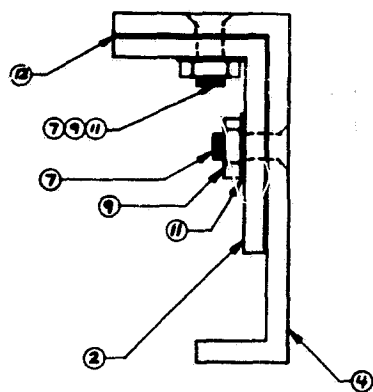
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D

C

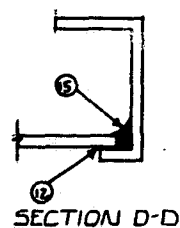
B

A

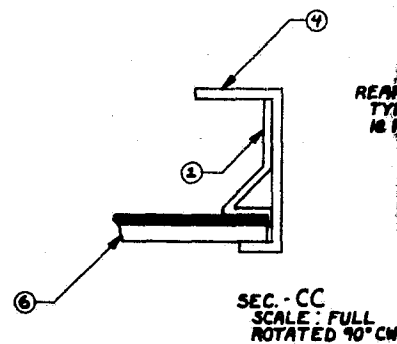


SEC - AA
SCALE 2:1

L-SUPPORT ASS'Y
POSITIONED ON E OF FRAME SPLIT



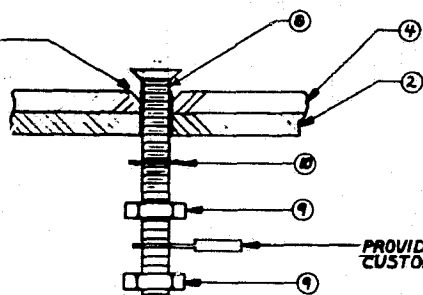
SECTION D-D



SEC - CC
SCALE: FULL
ROTATED 90° CW

GROUND PROVISION ASS'Y

4.2mm Max Depth
AFTER FRAME
ANODIZING



SEC BB
SCALE 2:1

PROVIDED BY
CUSTOMER

FOLDOUT FRAME

8

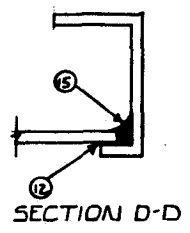
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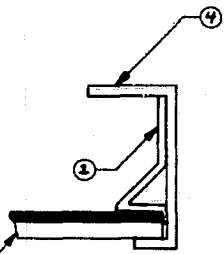
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5 4 3 2 1

REVISIONS			
ZONE	LTR	DESCRIPTION	DATE
	A	GEN REVISION	12/4/77 JFH
	B	GEN REVISION	9/10/80 JFH
	C	GEN. REVISION	11/26/80 JFH
	D	ADD ASSY NOTE, SEE D-D	12/20/80 JFH
	E	EXPAND ASSY NOTE	1/30/81 JFH
	F	ADD PROV. FOR MODIFICATION - LABEL	3/2/81 JFH
	G	CHANGE A NOTE	3/2/81 JFH



DETAIL SHOWING
ADHESIVE AND
MODULE-FRAME SEAL



SEC. CC
SCALE: FULL
ROTATED 90° CW

FOLDOUT FRAME 2

QTY REQD	CODE IDENT	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES = .XX = ° =			CONTRACT NO.	
MATERIAL			APPROVALS DATE	
FINISH			DRAWN P. MCGHEE 11/5/77	
NEXT ASSY USED ON			CHECKED J. H. L. 12/3/77	
APPLICATION			DO NOT SCALE DRAWING	
			SOLAREX CORPORATION 1335 PICCARD DRIVE ROCKVILLE, MD 20850 □ 301 948 0202	
			BLOCK II - INTERMEDIATE LOAD TOP ASSEMBLY	
			SIZE CODE IDENT NO. DRAWING NO. D RID 00616	
			SCALE AS SHOWN SHEET 2 OF 2	

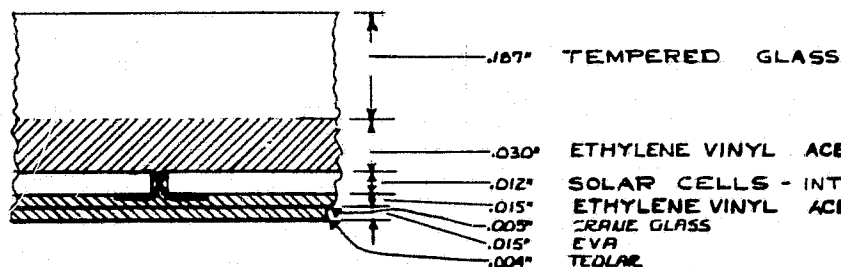
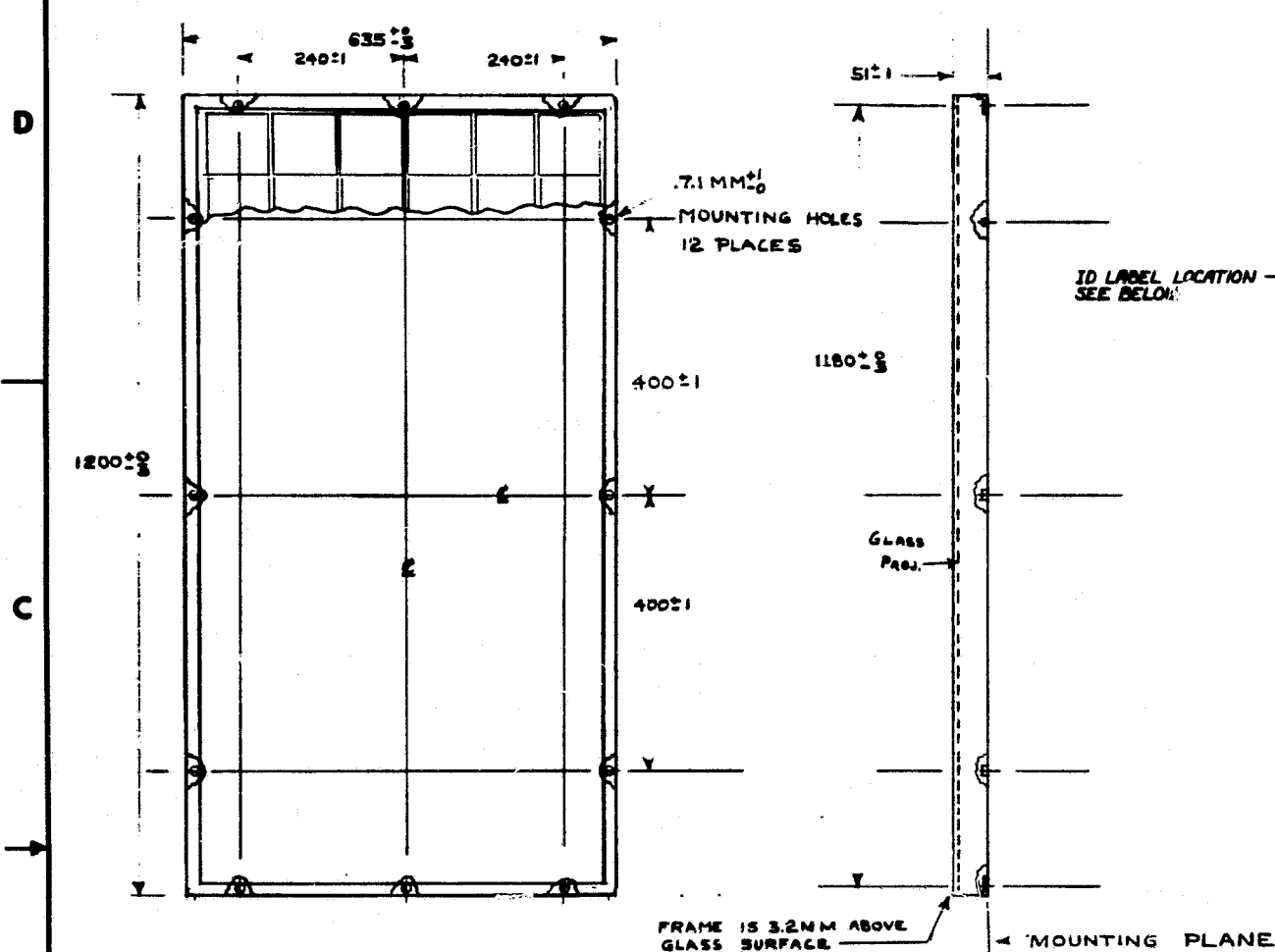
5 4 3 2 1

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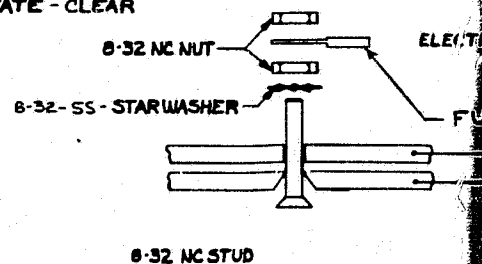
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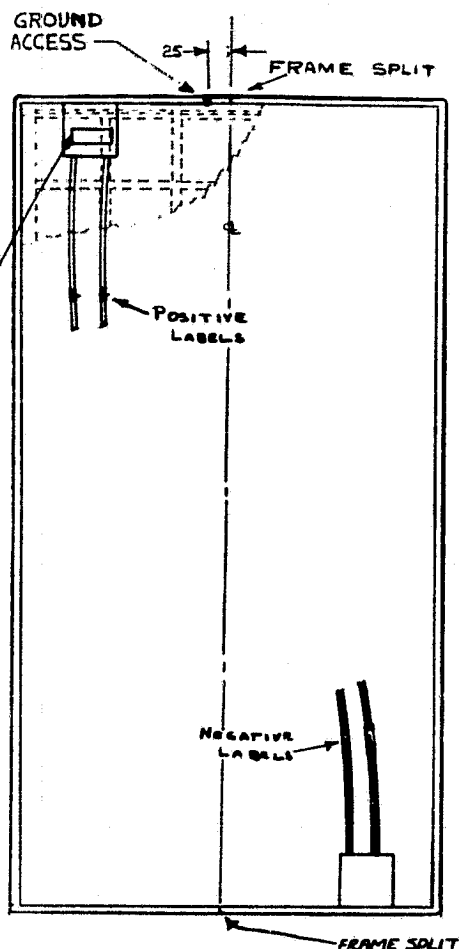


MODEL NO.
 580-BT-L-B 500V/A
 SERIAL NO.
 YV 22
 SOLAREX CO.

IDENTIFICATION



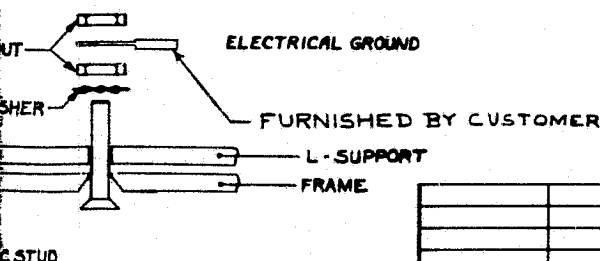
FOLDOUT FRAME



REAR VIEW
VIEW SHOWING LOCATION OF TERMINAL BOXES
AND GROUNDING PROVISION

MODEL NO.	
580-BT-L-B 500V MAX.	
SERIAL NO.	
XXXXXX	YY ZZ
SOLAREX CORP.	

IDENTIFICATION LABEL



APPLICATION	USED ON

QTY REQD	CODE IDENT	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION
PARTS LIST			
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES-M.M TOLERANCES ARE: FRACTIONS DECIMALS ANGLES ± .XX ± ± ± .XXX ± ±		CONTRACT NO.	
MATERIAL FRAME AL ALLOY 6063-752		APPROVALS DATE DRAWN 11/12/79 CHECKED	
FINISH BLUE ANODIZE		SIZE CODE IDENT NO. DRAWING NO. D RID 0862F	
NEXT ASSY USED ON		Section 8.2	
APPLICATION		DO NOT SCALE DRAWING	
		SCALE	
		SHEET 1 OF 1	

REVISIONS			
ZONE	LTR	DESCRIPTION	DATE
A.		GENERAL REV.	1/7/80
B.		GENERAL REV.	4/18/80
C.		GENERAL REV.	11/14/80
D.		CLARIFY NOTES	1/30/81
E.		ADD PROV FOR MODIFICATION LABEL	3/2/81
F.		CHANGE LABEL REV. CONVENTION	3/19/81

TWO REDUNDANT POSITIVE AND NEGATIVE #14 WIRES FROM EACH OF TWO TERMINAL BOXES BLACK-NEG. WHITE-POS.

ACTIVE (ILLUMINATED) SURFACE DIMENSIONS - 603 MM x 1160MM

NO SHADOWING WHEN SUN ANGLE IS GREATER THAN 41° LONG DIMENSION OR 45° SHORT DIMENSION

MAX WT-35 LBS

MODULE SHALL NOT BE DEFLECTED MORE THAN 20 MM PER METER

ATTACH ELECTRICAL GROUND AS SHOWN - MAX TORQUE 30 IN-LBS

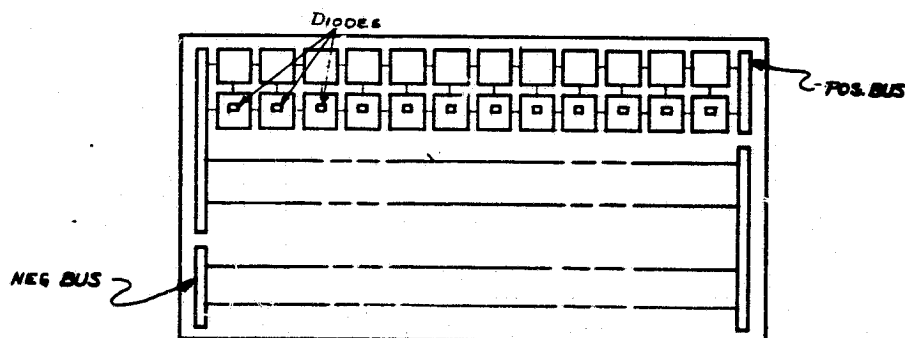
MOUNT WITH #8-32 x 1 1/4 NC MACHINE SCREW

IDENTIFICATION LABEL ON POS TERMINAL BOX TOP TOWARD SHORT DIMENSION

POS. TERMINAL NEAREST TO GROUND PROVISION-
XXXXXX-SEQUENTIAL SERIAL NO.
YY-CALENDAR YEAR
ZZ-WEEK IN CALENDAR YEAR

ALL WIRE LENGTHS - 30 cm FROM TERMINAL BOX

IN MODEL NO. BLOCK 580-BT-L IS MODEL DESIGNATION
-B IS CURRENT REVISION NO. ON DRAWING LIST



CELL ORIENTATION (FROM BACK)
(NOT TO SCALE) - 1 DIODE PER
TWO PARALLELED CELLS

FOLDOUT FRAME

9.0 Conclusions:

Semicrystalline silicon cells were no more difficult to work with than single crystal cells.

The wraparound feature of the tabbing-stringing scheme worked reliably. There had been concern expressed that during lamination the tabs would come in contact with the edge of cells. We have not found that to be a problem.

The in-plane stress relief designed into the interconnect worked well. No interconnect failures were noted in any of the pre-production Block IV modules nor have we noted any interconnect failures in those modules of this design which are in the field.

We learned to work with EVA. Using the experience gained in the Block IV program, Solarex is now routinely making finished panels using EVA.

The crack-tolerant cell design was recognized by JPL when JPL approved the inspection system plan. As many as seven different cells per module could have cracks (some with double cracks) and still be regarded as acceptable.